

FIG.1

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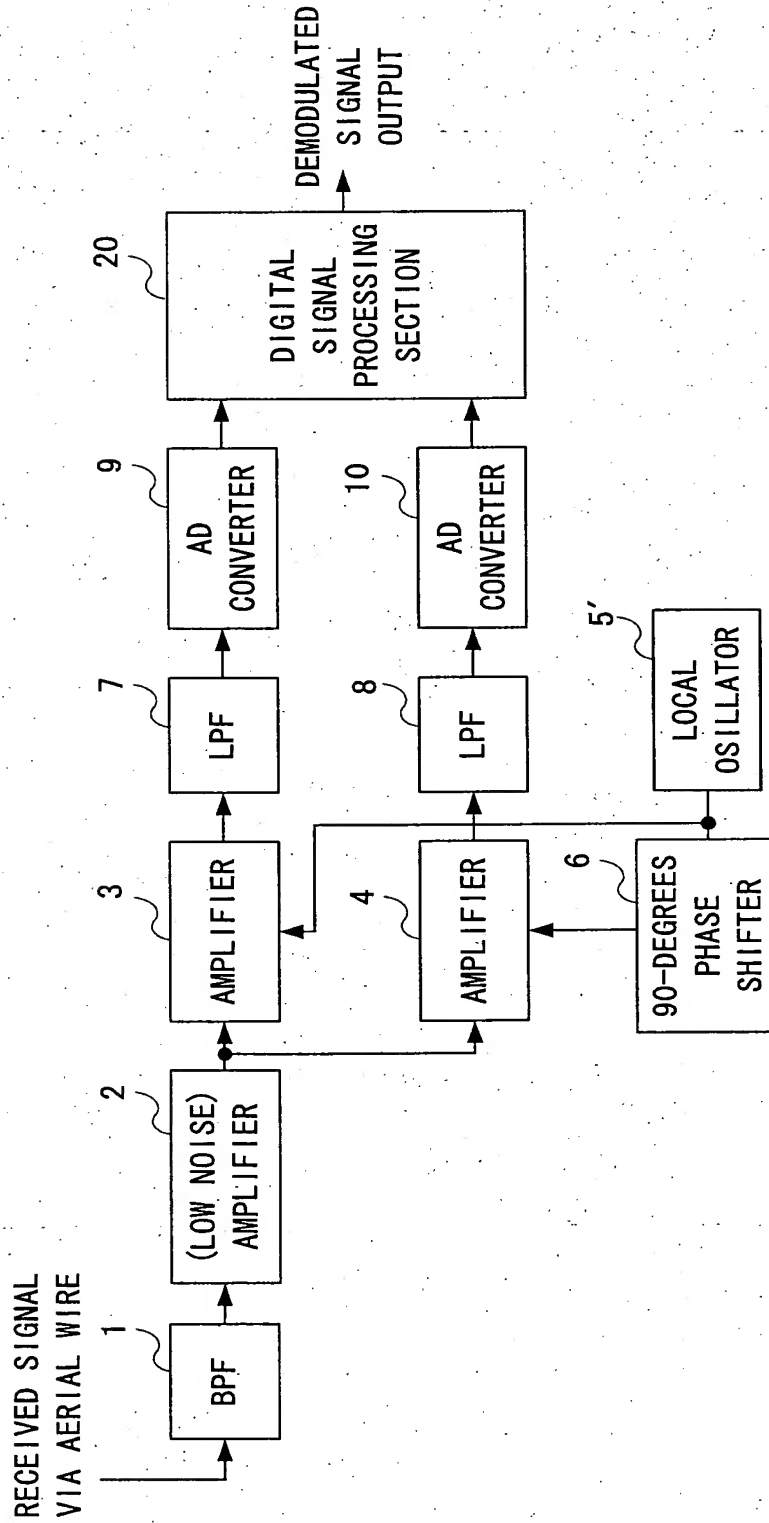


FIG.2

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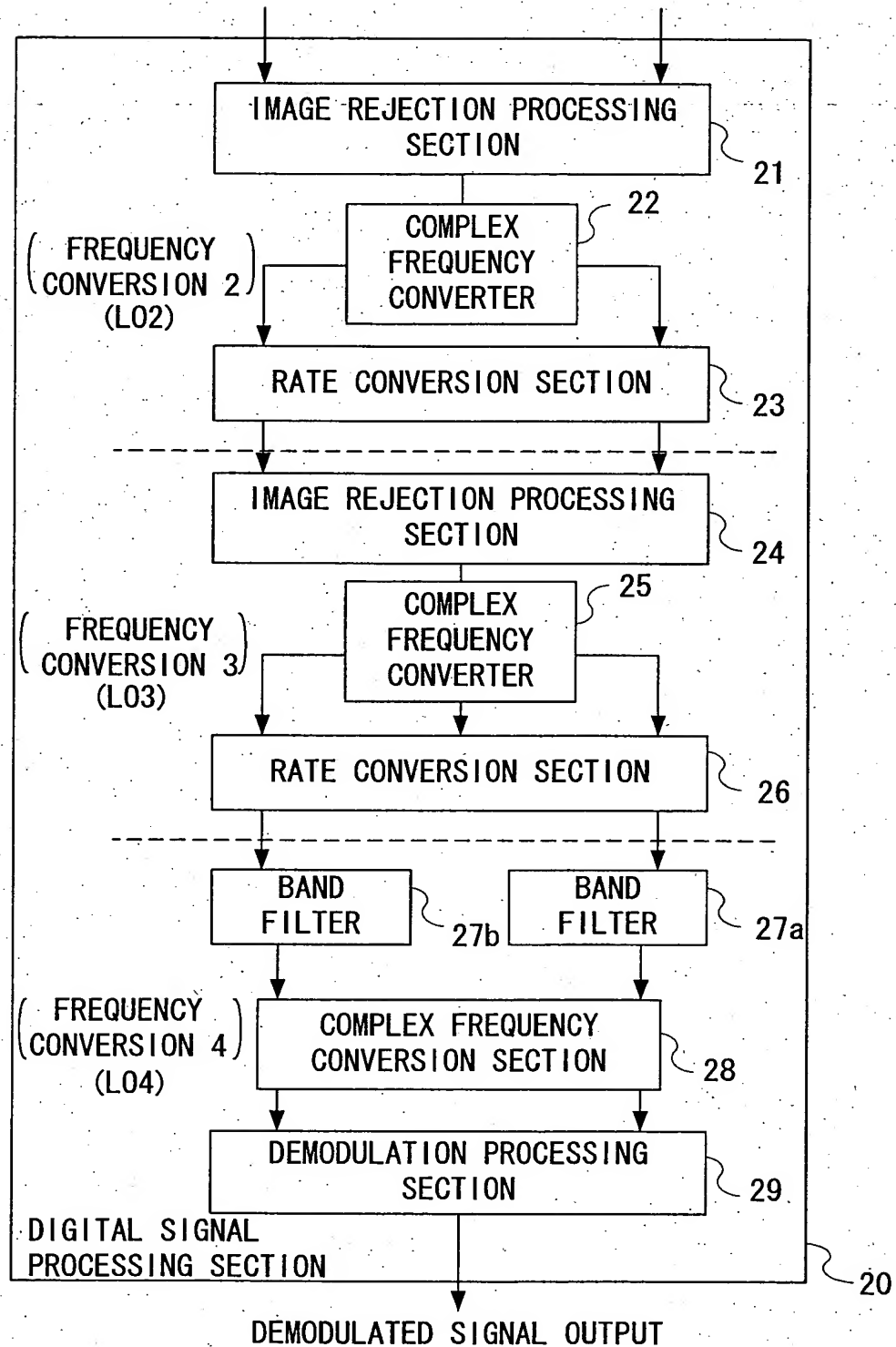


FIG.3A SIGNAL AFTER FREQUENCY CONVERSION PROCESSING BY ANALOG PROCESSING (LOCAL OSCILLATOR)

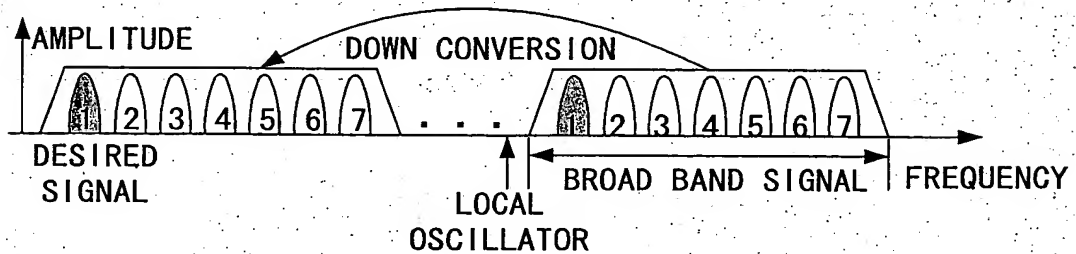


FIG.3B SIGNAL AFTER AD CONVERSION

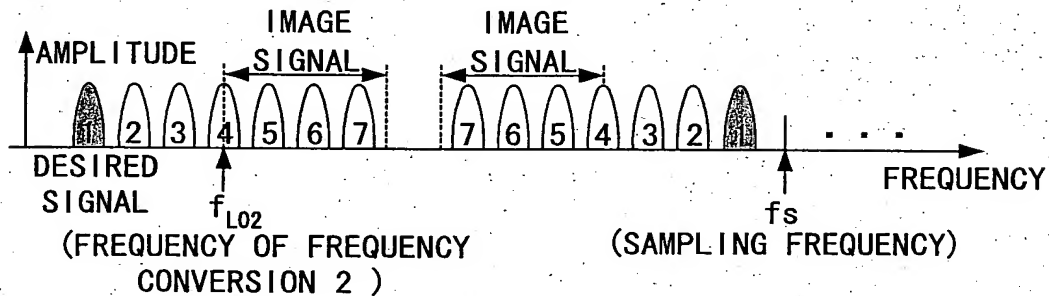


FIG.3C SIGNAL AFTER IMAGE REJECTION PROCESSING, COMPLEX FREQUENCY CONVERSION, RATE CONVERSION PROCESSING (FIRST)

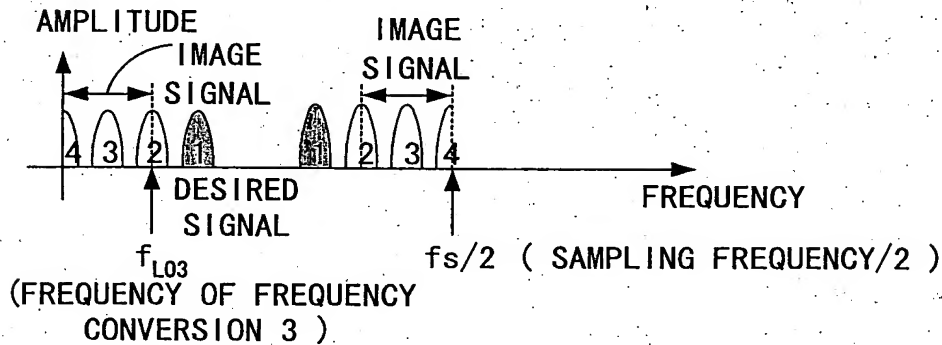


FIG.3D SIGNAL AFTER IMAGE REJECTION PROCESSING, COMPLEX FREQUENCY CONVERSION, RATE CONVERSION PRECESSING (SECOND)

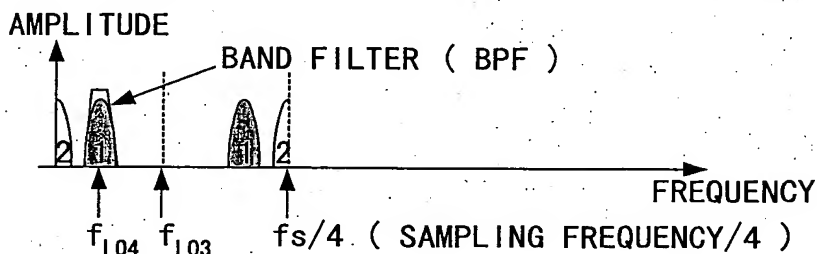


FIG.4

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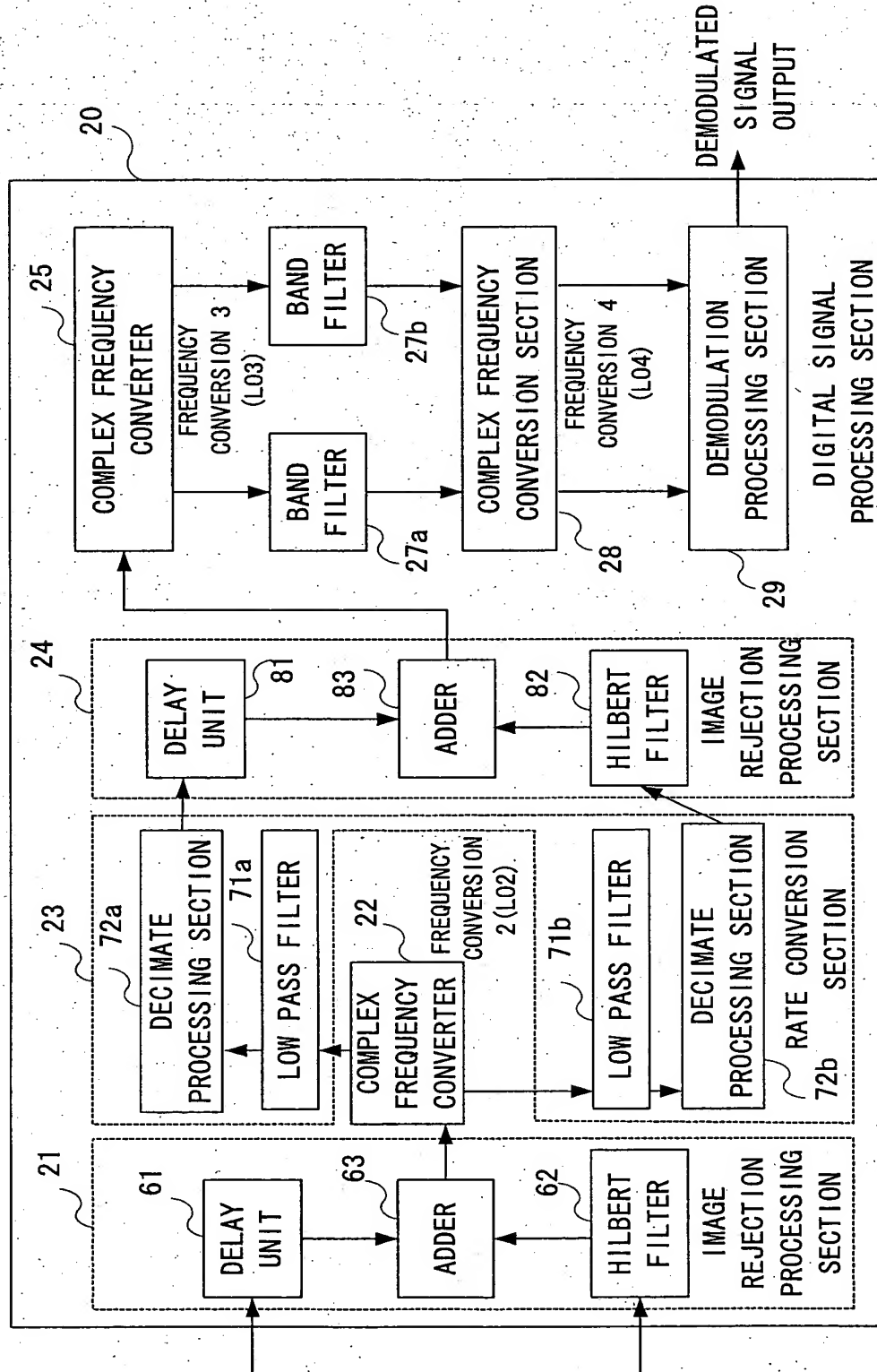


FIG.5

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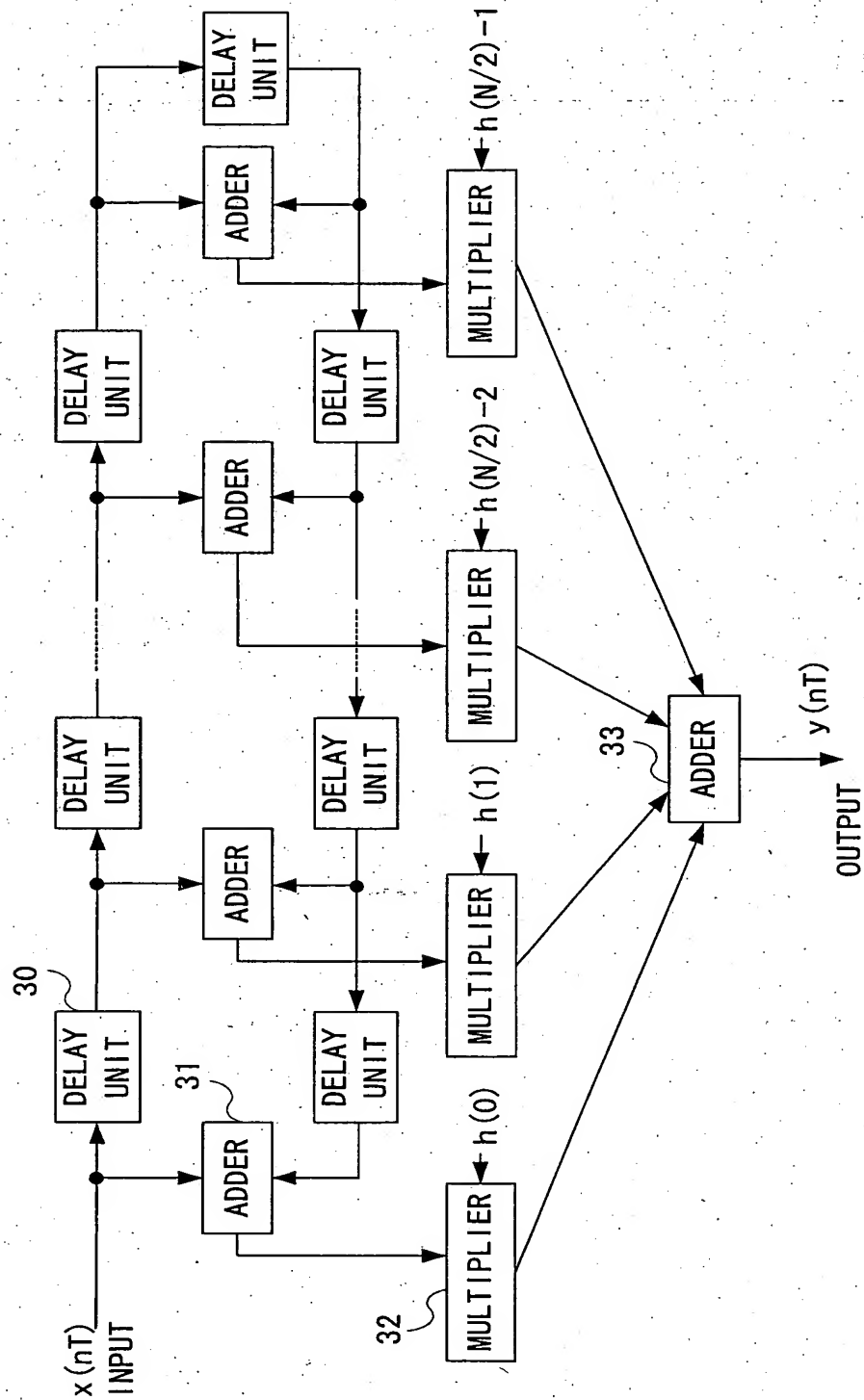


FIG.6

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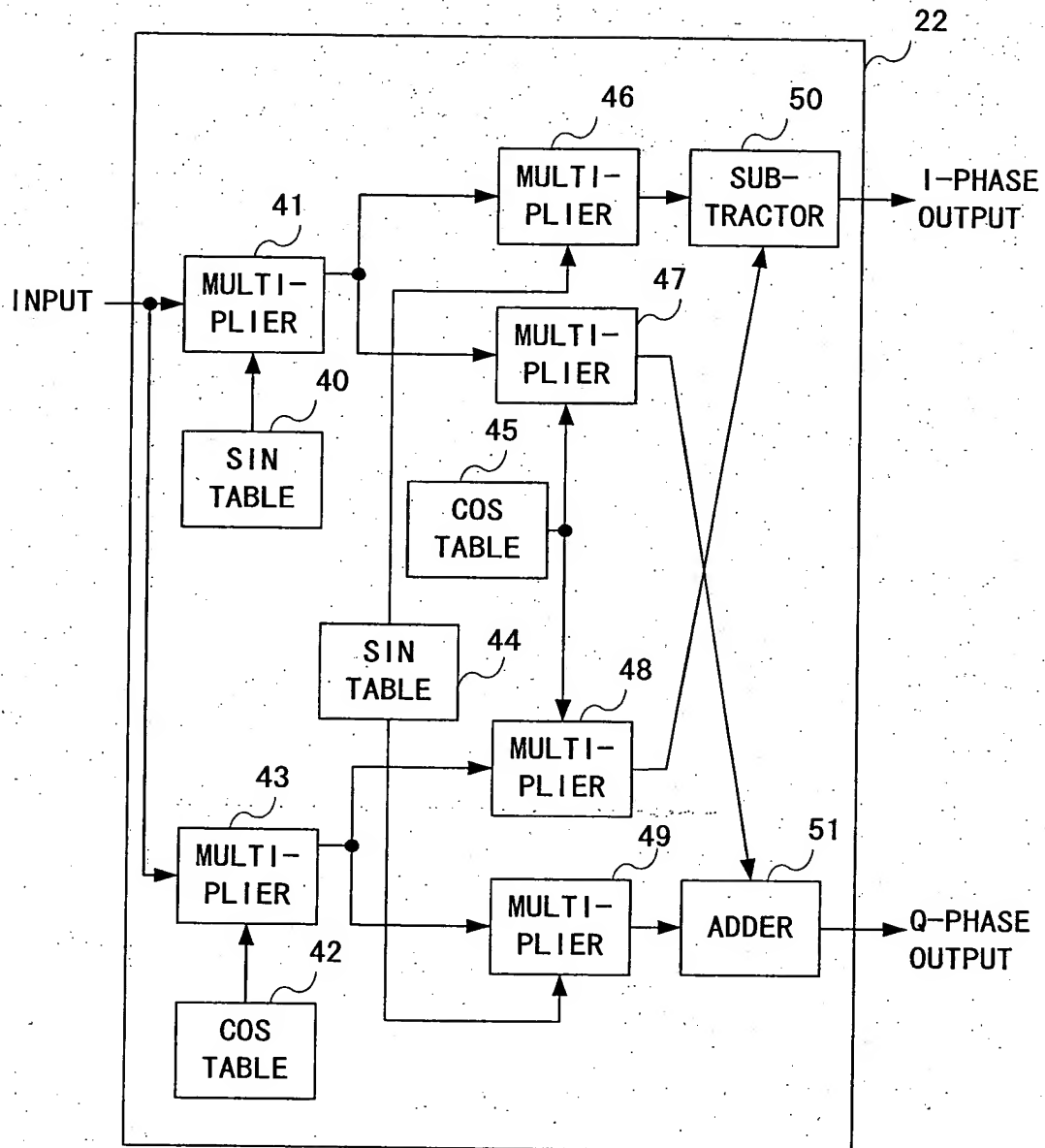


FIG. 7

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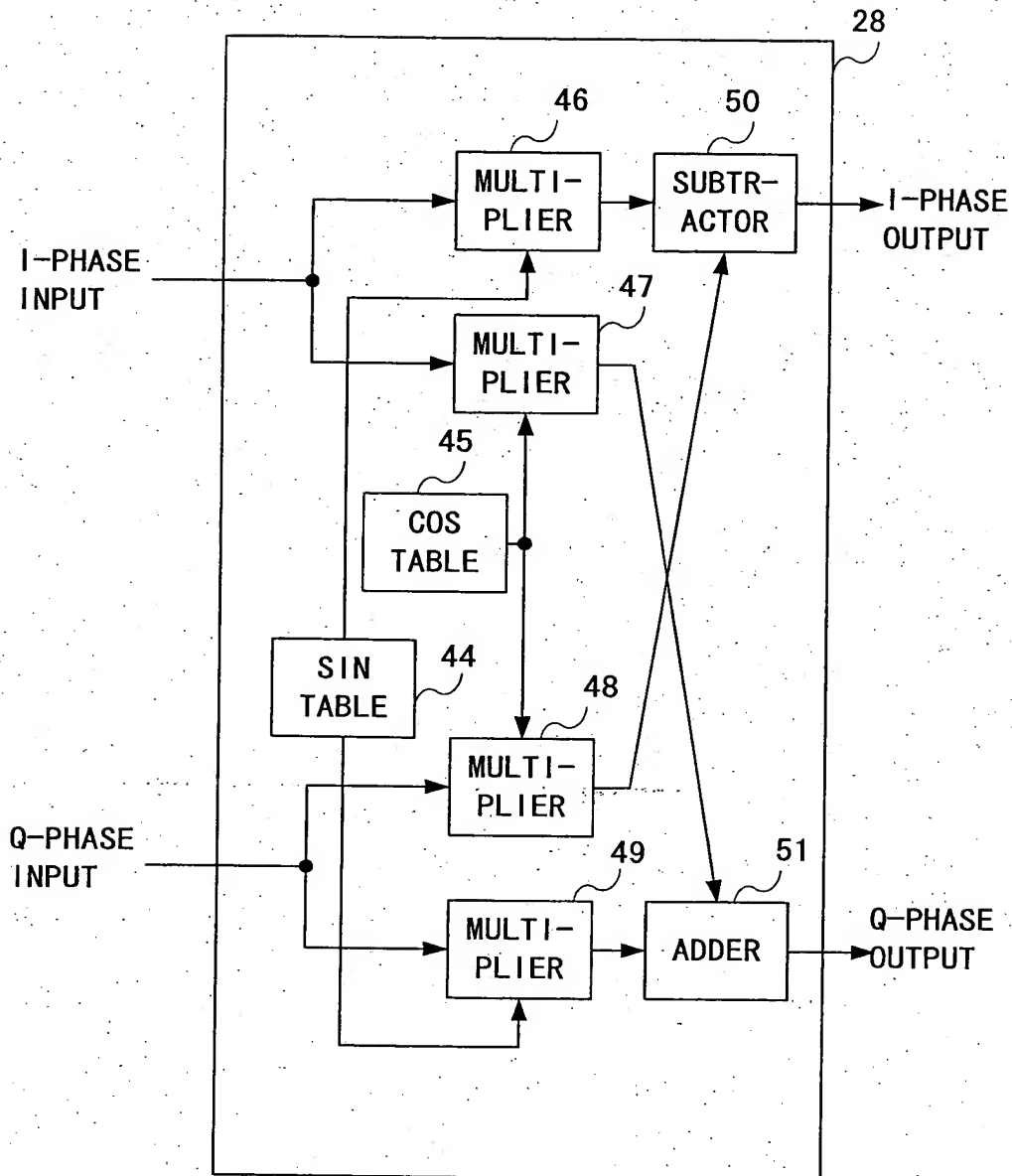


FIG.8

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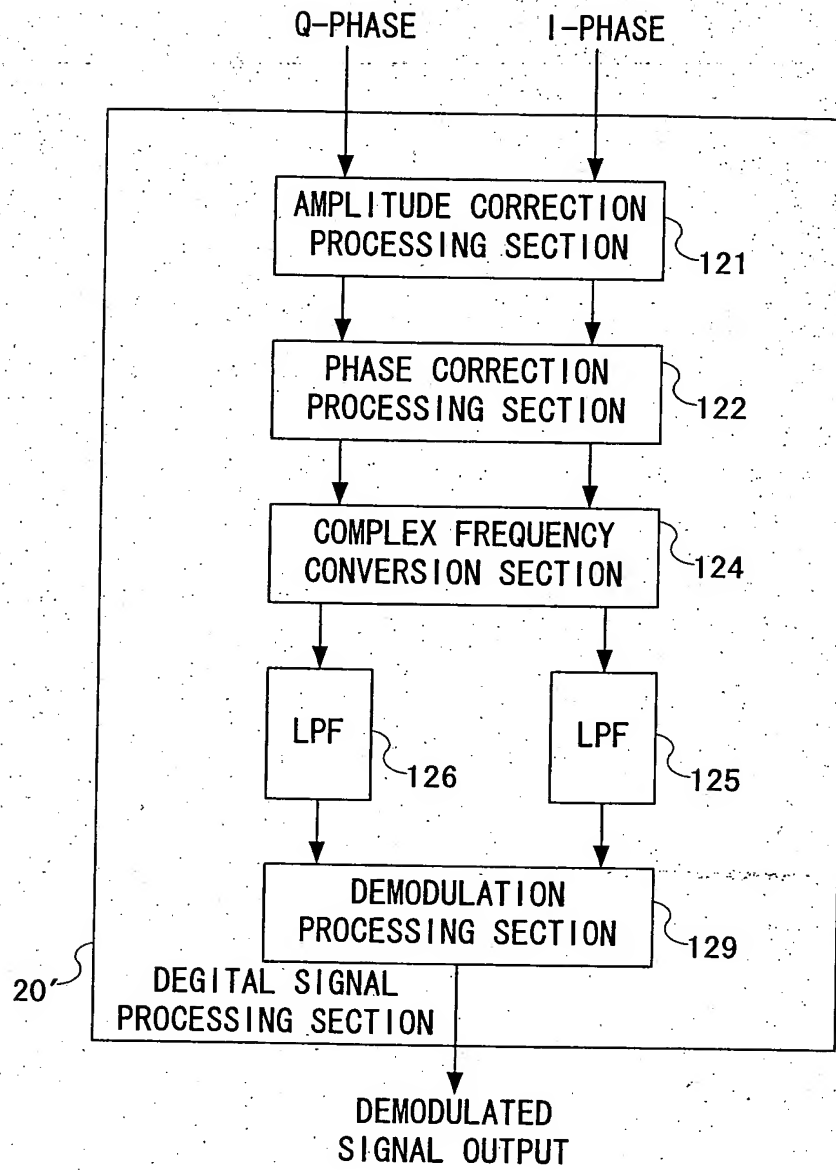


FIG.9

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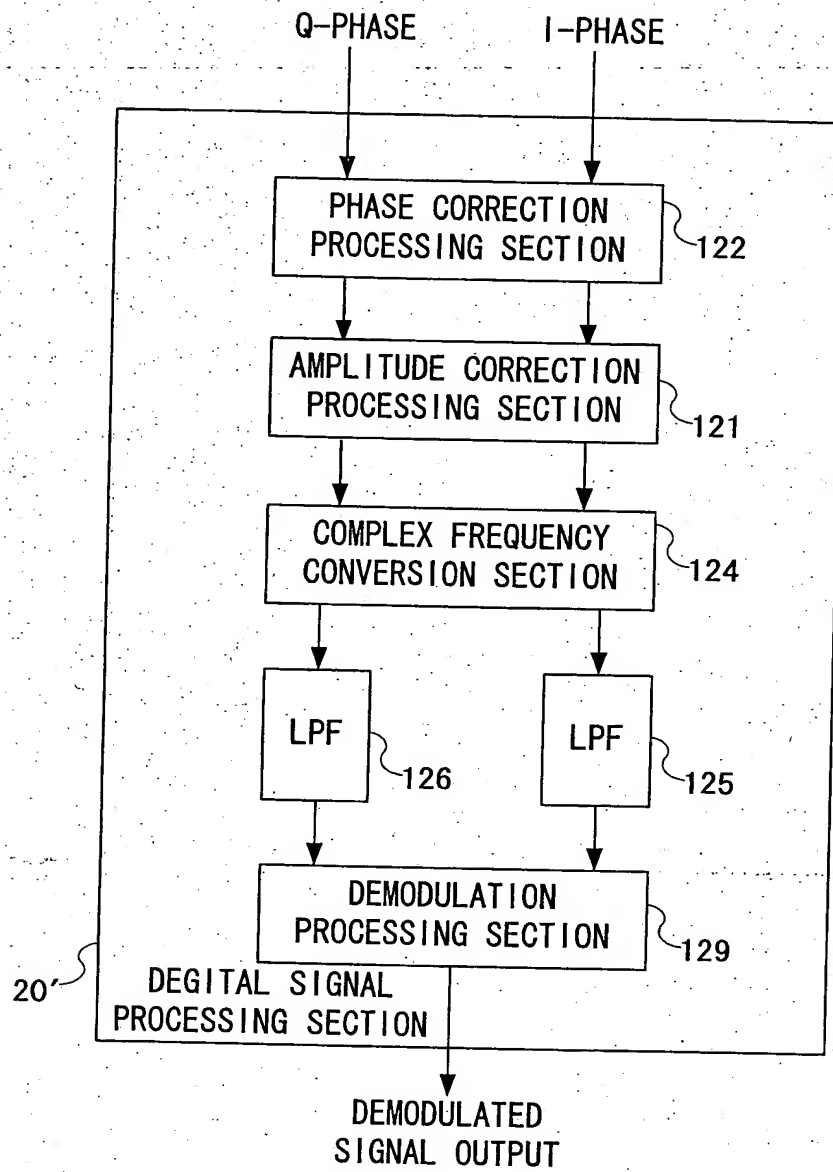


FIG.10

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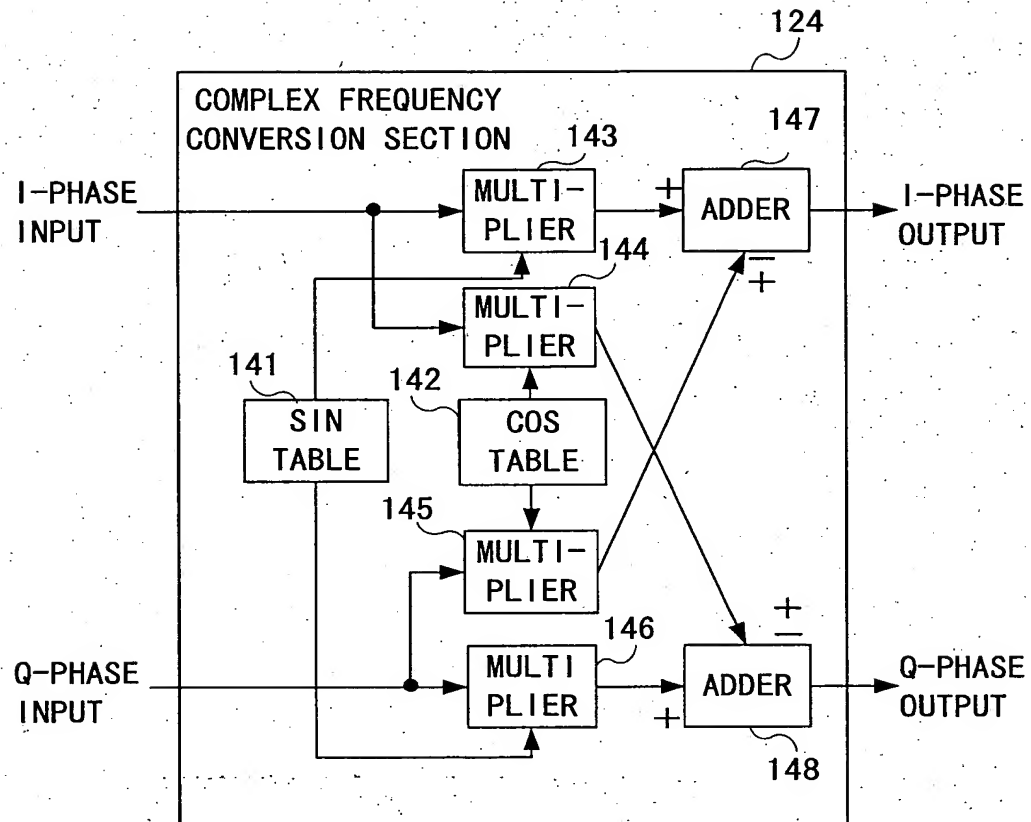


FIG.11

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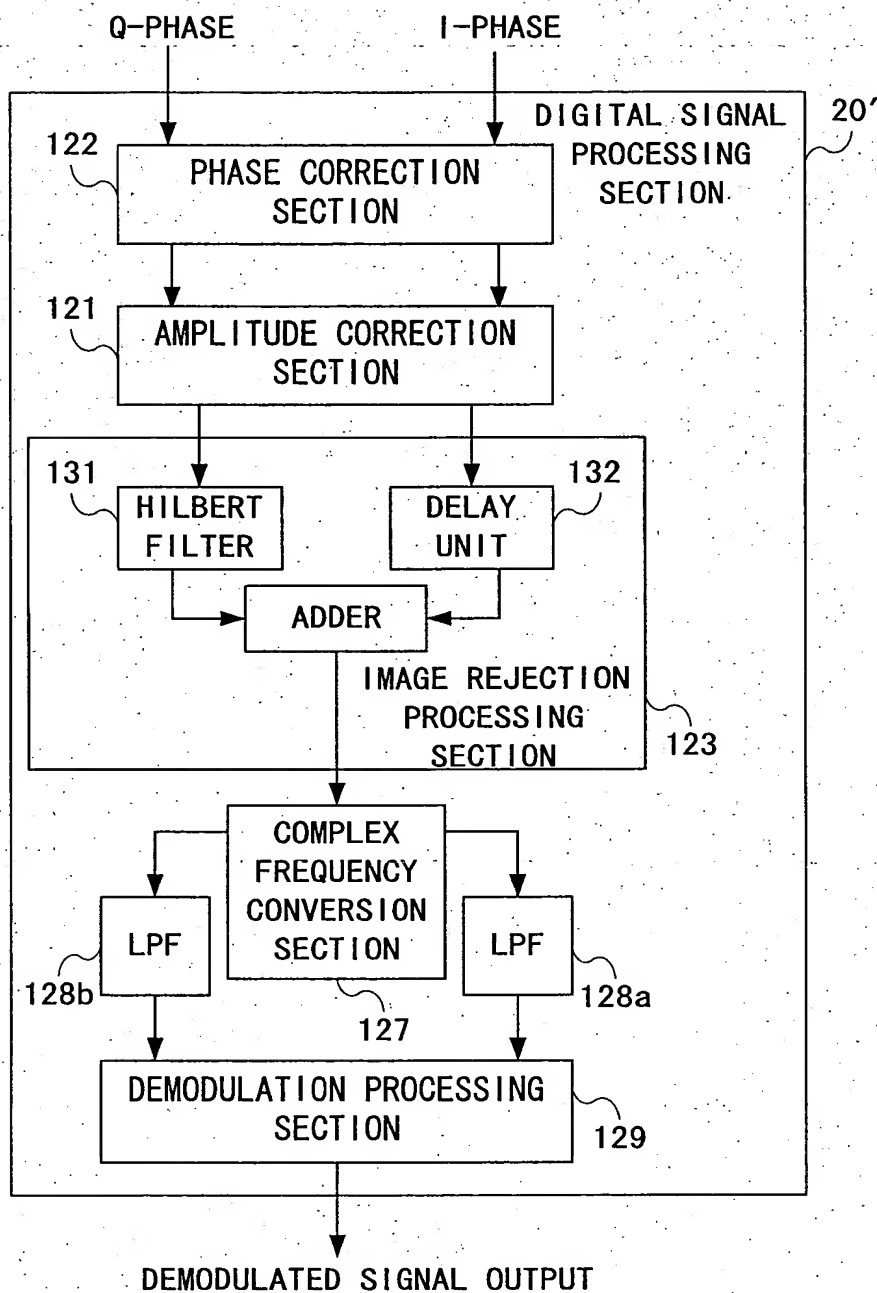


FIG.12

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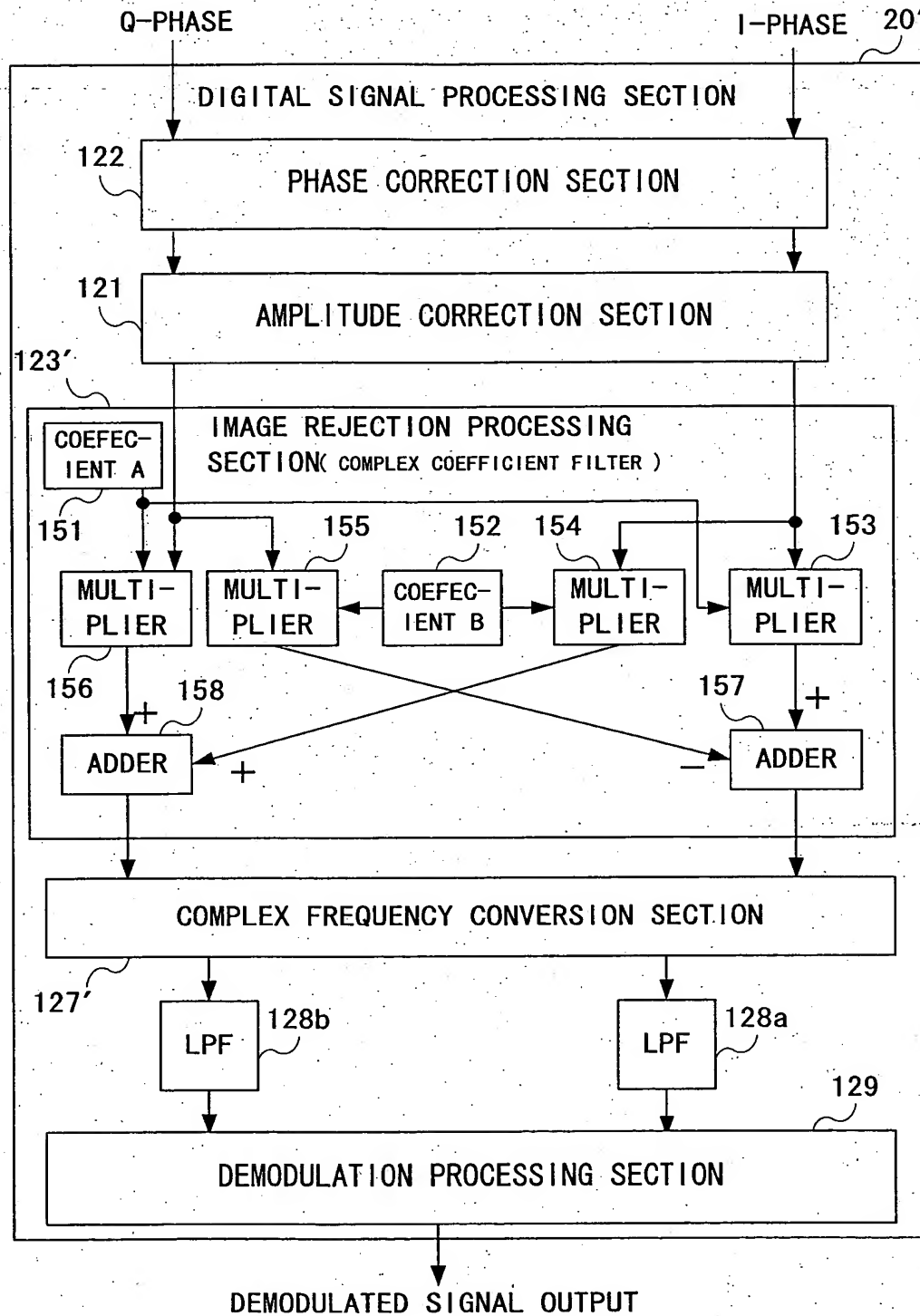


FIG.13

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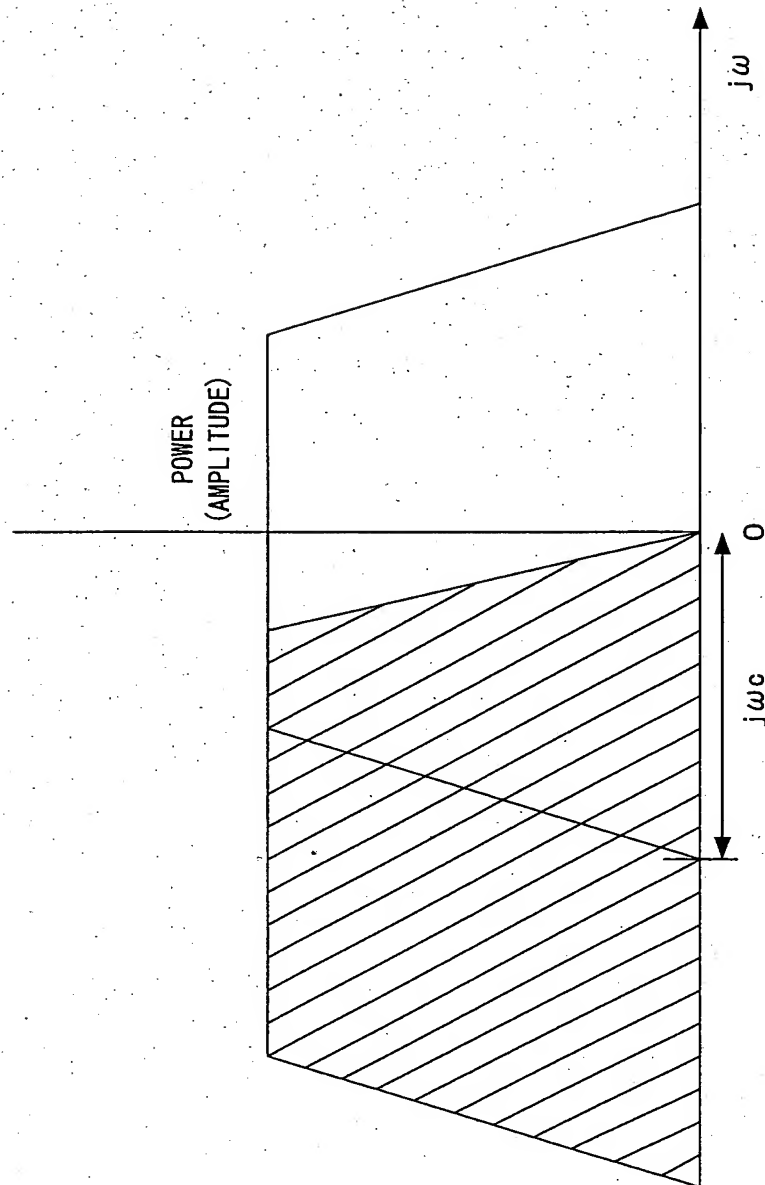


FIG.14

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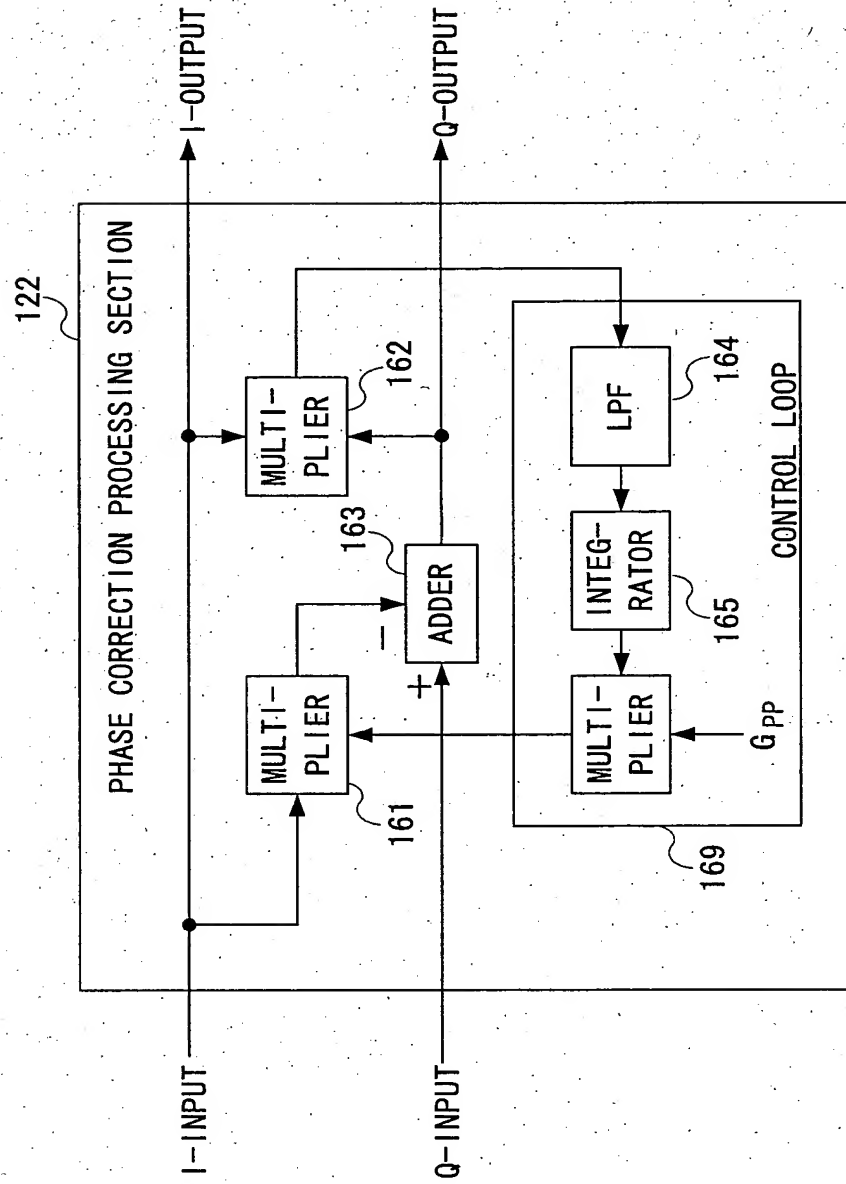


FIG.15

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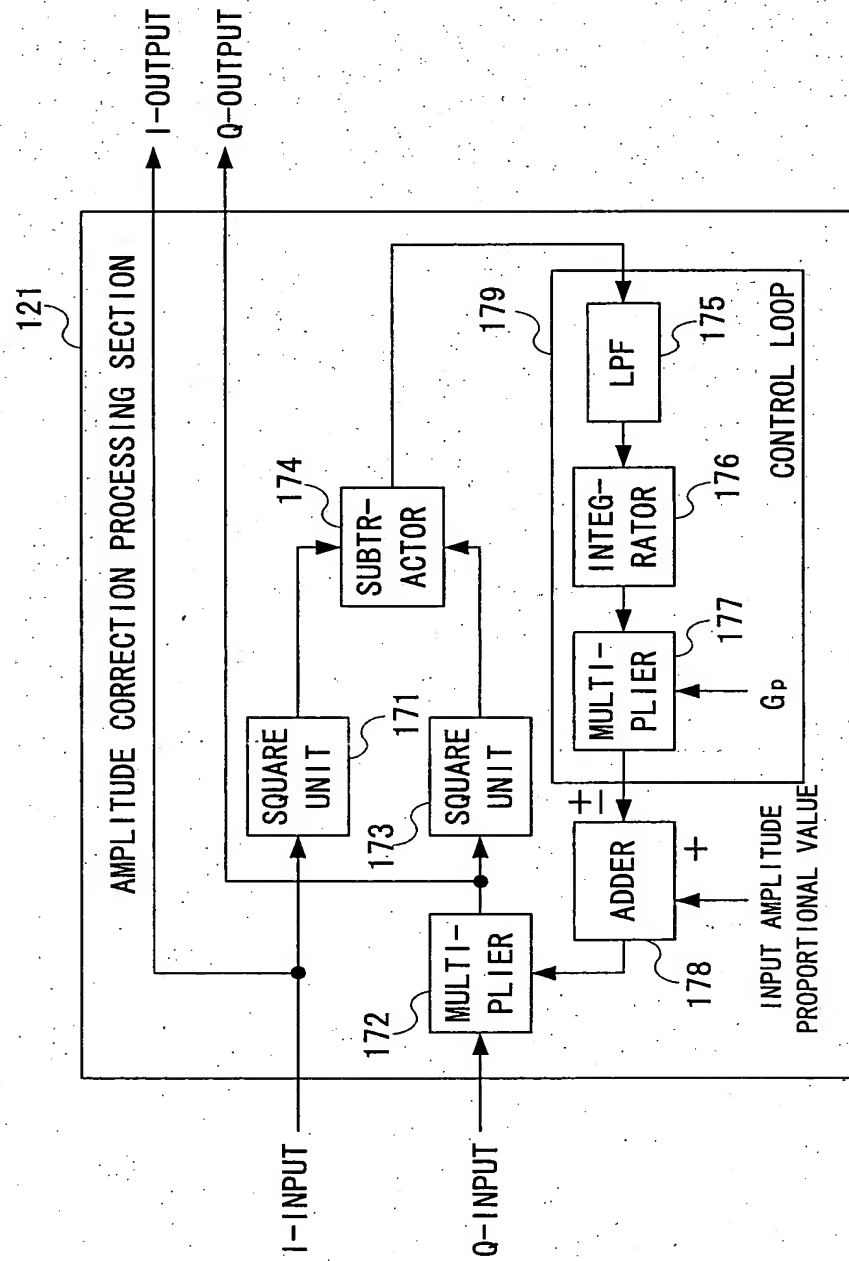
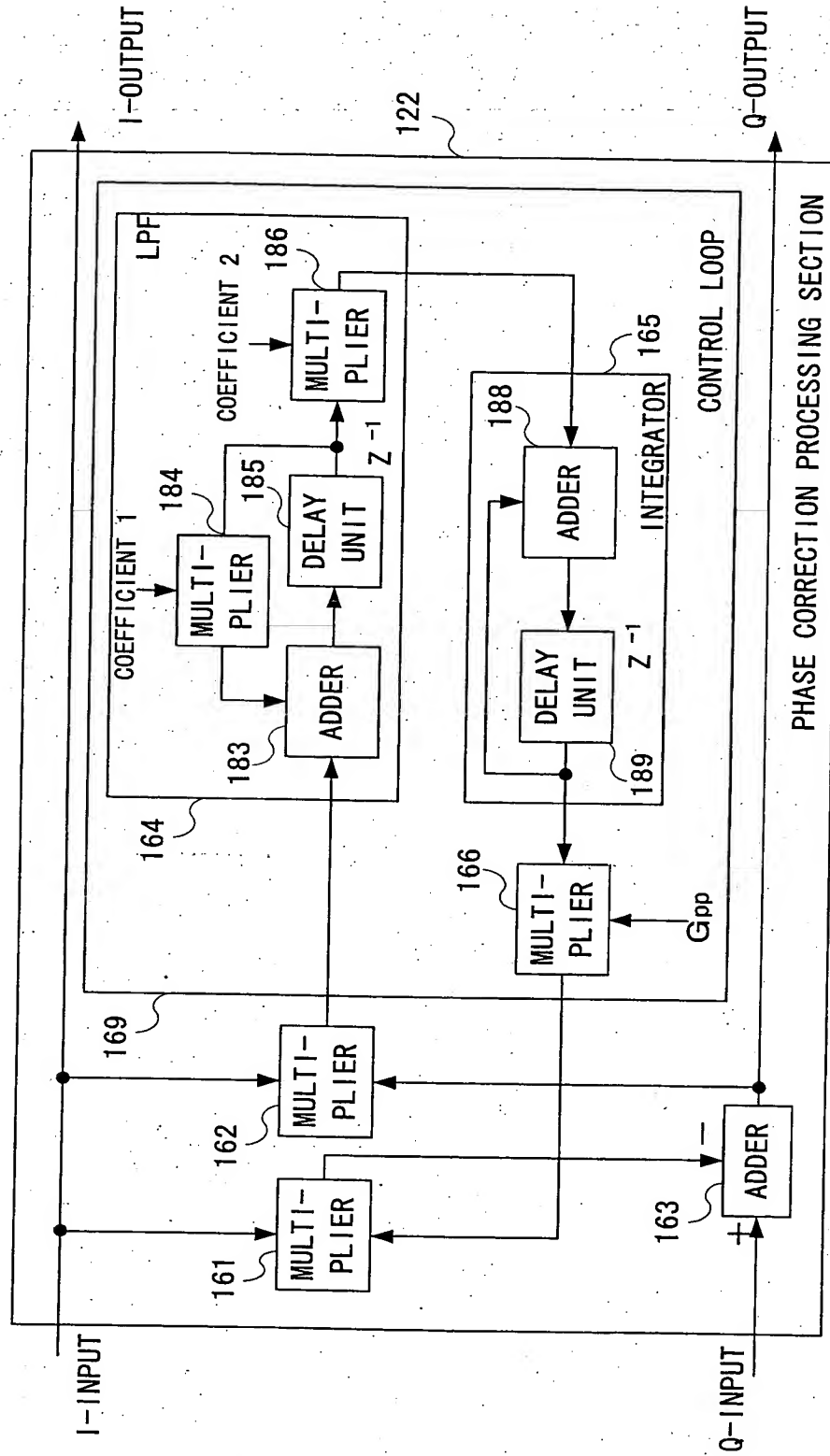


FIG.16

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